

**Identification**

DTM60126:128Mx72

Performance range

133MHz (7.5ns@ CL=3)

Features

Burst mode operation

Auto & self refresh capability (8192 Cycles/64ms)

LVTTL compatible inputs and outputs

Single 3.3V±0.3V power supply

MRS cycle with address key programs Latency (Access from column address) Burst length (1, 2, 4 & 8 page) Data scramble (Sequential & Interleave)

Serial presence detect with EEPROM

168-pin PC133 DIMM double-sided assembly
5.250" wide by 1.70" high

Description

The Dataram DTM60126 is a registered 128Mx72 and Synchronous Dynamic RAM high density memory module. The assembly consists of eighteen stacked 128Mx4bit SDRAMs in two TSOP-II 400MII packages. Three 18-bits Drive ICs for input control, one PLL for clock, and one 2K EEPROM for Serial Presence Detect are also installed on a 168-pin glass-epoxy substrate. Synchronous design allows for precise cycle control with the use of a system clock. The DTM60126 is a Dual in-line Memory Module intended for mounting into 168-pin edge connector sockets.