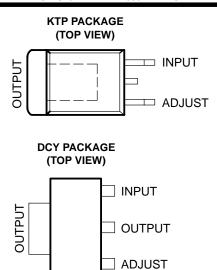
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- Output Voltage Range Adjustable From 1.25 V to 37 V
- Output Current Greater Than 500 mA
- Internal Short-Circuit Current Limiting
- Thermal-Overload Protection
- Output Safe-Area Compensation
- Q Devices Meet Automotive Performance Requirements
- Customer-Specific Configuration Control Can Be Supported for Q Devices Along With Major-Change Approval

### description/ordering information

The LM317M is an adjustable 3-terminal positive-voltage regulator capable of supplying more than 500 mA over an output-voltage range of 1.25 V to 37 V. The LM317M is exceptionally easy to use and requires only two external resistors to set the output voltage. Furthermore, both line and load regulation are better than standard fixed regulators.



In addition to having higher performance than fixed regulators, the device includes on-chip current limiting, thermal-overload protection, and safe-operating-area protection. All overload protection remains fully functional if the ADJUST terminal is disconnected.

Normally, no capacitors are needed unless the device is more than 6 inches from the input filter capacitors, in which case an input bypass capacitor is needed. An optional output capacitor can be added to improve transient response. The ADJUST terminal can be bypassed to achieve very high ripple-rejection ratios, which are difficult to achieve with standard three-terminal regulators.

Tj	PACKAGE <sup>†</sup>		ORDERABLE PART NUMBER	TOP-SIDE MARKING
0°C to 125°C	POWER-FLEX (KTP)	Reel of 2000	LM317MKTPR	LM317M
	SOT (DCY)	Tube of 80	LM317MDCY	14
		Reel of 2500	LM317MDCYR	L4
-40°C to 125°C	POWER-FLEX (KTP)	Reel of 2000	LM317MQKTPR	317MQ
	SOT (DCY)	Reel of 2500	LM317MQDCYR	L5

#### **ORDERING INFORMATION**

<sup>†</sup> Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



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### absolute maximum ratings over operating temperature range (unless otherwise noted)<sup>†</sup>

Input-to-output differential voltage, V <sub>I</sub> – V <sub>O</sub>	40 V
Operating virtual junction temperature, T <sub>J</sub> 1	50°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	28°C
Storage temperature range, T <sub>stg</sub>	50°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

### package thermal data (see Note 1)

PACKAGE	BOARD	θJC	θJA
POWER-FLEX (KTP)	High K, JESD 51-5	19°C/W	28°C/W
SOT-223 (DCY)	High K, JESD 51-7	4°C/W	53°C/W

NOTE 1: Maximum power dissipation is a function of  $T_J(max)$ ,  $\theta_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any allowable ambient temperature is  $P_D = (T_J(max) - T_A)/\theta_{JA}$ . Operating at the absolute maximum  $T_J$  of 150°C can affect reliability.

### recommended operating conditions

			MIN	MAX	UNIT
VI – VO	VI – VO Input-to-output voltage differential			37	V
IO	IO Output current			0.5	А
Тј	Operating virtual junction temperature	No suffix	0	125	°C
		Q suffix	-40	125	

# electrical characteristics over recommended operating virtual-junction temperature range, $V_I - V_O = 5 V$ , $I_O = 0.1 A$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS <sup>‡</sup>		MIN	TYP	MAX	UNIT
1 /	$V_{I} - V_{O} = 3 V$ to 40 V	T <sub>J</sub> = 25°C		0.01	0.04	%/V
Line regulation§		Full temperature range		0.02	0.07	
Load regulation	I <sub>O</sub> = 10 mA to 500 mA	T <sub>J</sub> = 25°C		0.1	0.5	%VO
Load regulation		Full temperature range		0.3	1.5	
ADJUST terminal current				50	100	μA
Change in ADJUST terminal current	$V_{I} - V_{O} = 3 V \text{ to } 40 V,$	I <sub>O</sub> = 10 mA to 500 mA		0.2	5	μA
Reference voltage	$V_{I} - V_{O} = 3 V \text{ to } 40 V,$	I <sub>O</sub> = 10 mA to 500 mA	1.2	1.25	1.3	V
Output-voltage temperature stability				0.7%		
Minimum load current to maintain regulation				3.5	10	mA
	$V_I - V_O \le 15 V$		500	900		
Maximum output current	$V_I - V_O = 40 V$ , $P_D \le P_D(max)$	$T_J = 25^{\circ}C$	150	250		mA
RMS output noise voltage (% of $V_{O}$ )	f = 10 Hz to 10 kHz,	TJ = 25°C		0.003		%VO
Binala asia sila s	V <sub>O</sub> = 10 V, f = 120 Hz, T <sub>J</sub> = 25°C	$C_{ADJ} = 0$ ¶		65		dB
Ripple rejection		$C_{ADJ} = 10 \ \mu F^{\text{I}}$	66	80		
Long-term stability	TJ = 125°C			0.3	1	%/ 1k Hrs

<sup>‡</sup> Pulse-testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible.

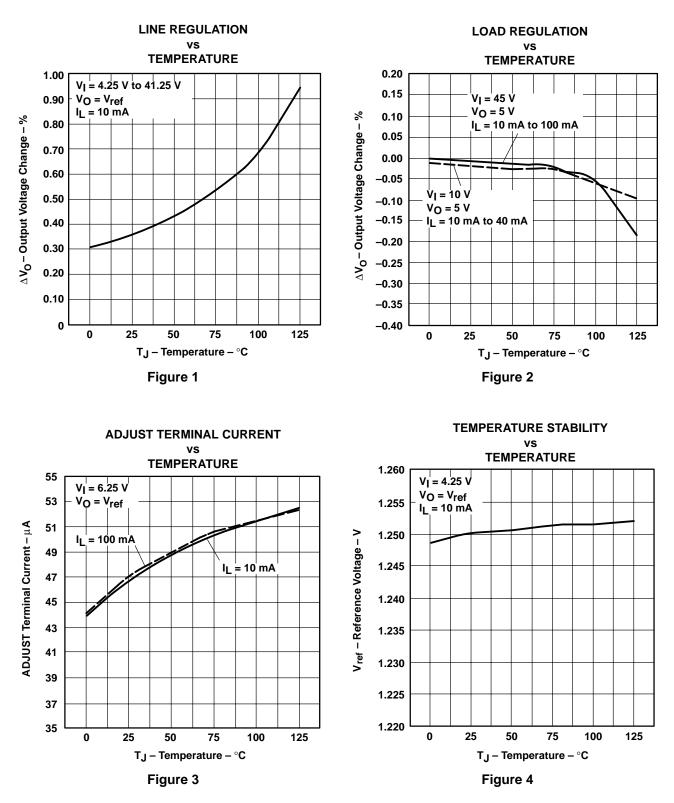
§ Line voltage regulation is expressed here as the percentage change in output voltage per 1-V change at the input.

 $\P$  C<sub>ADJ</sub> is connected between the ADJUST terminal and ground.



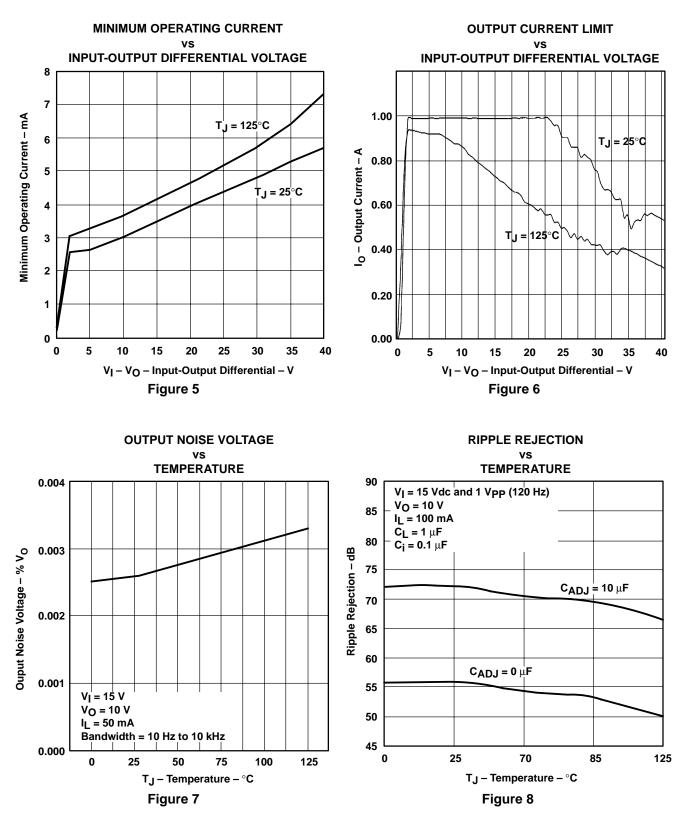
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### **TYPICAL CHARACTERISTICS**





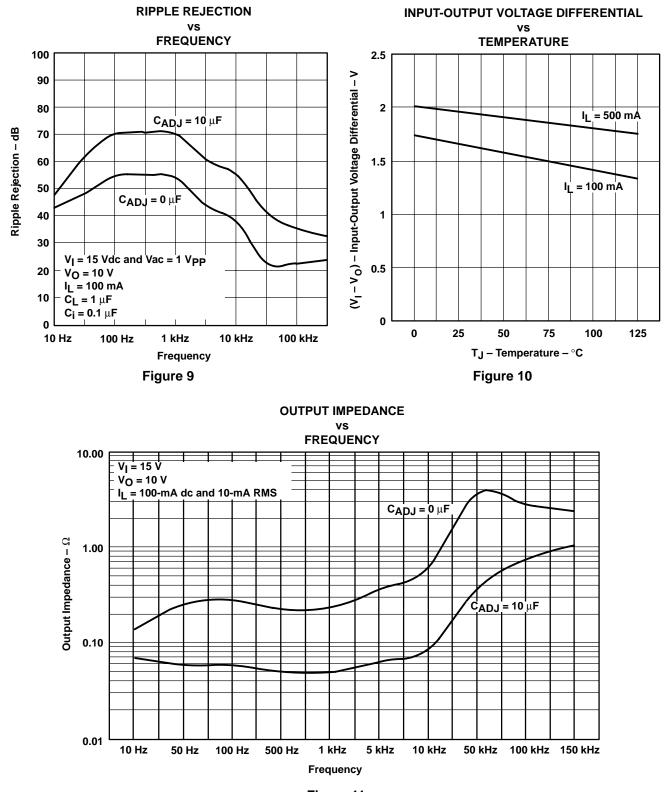
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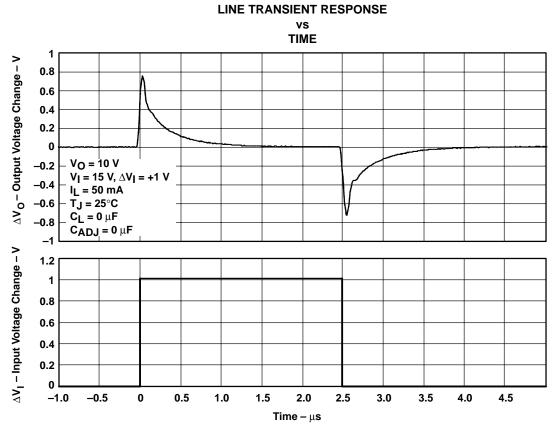


### **TYPICAL CHARACTERISTICS**

Figure 11



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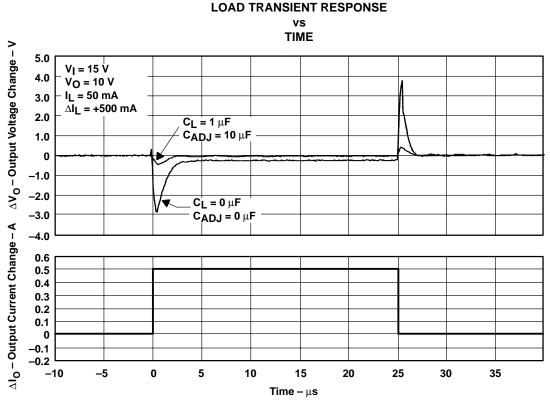


**TYPICAL CHARACTERISTICS** 

Figure 12



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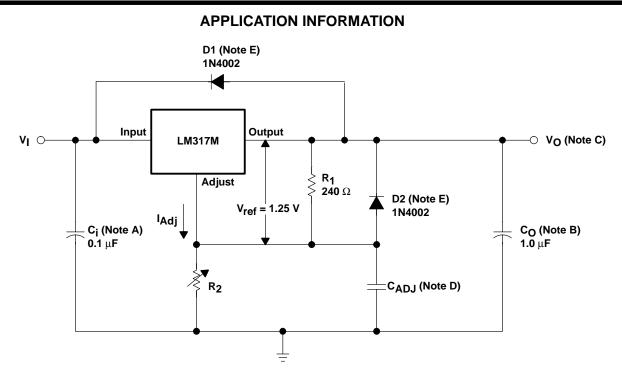


### TYPICAL CHARACTERISTICS

Figure 13



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- NOTES: A. C<sub>i</sub> is not required, but is recommended, particularly if the regulator is not in close proximity to the power-supply filter capacitors. A 0.1- $\mu$ F disc or 1- $\mu$ F tantalum provides sufficient bypassing for most applications, especially when adjustment and output capacitors are used.
  - B. CO improves transient response, but is not needed for stability.
  - C. V<sub>O</sub> is calculated as shown:

$$V_{O} = V_{ref} \left( 1 + \frac{R_2}{R_1} \right) + (I_{Adj} \times R_2)$$

Because  $I_{Adi}$  typically is 50  $\mu$ A, it is negligible in most applications.

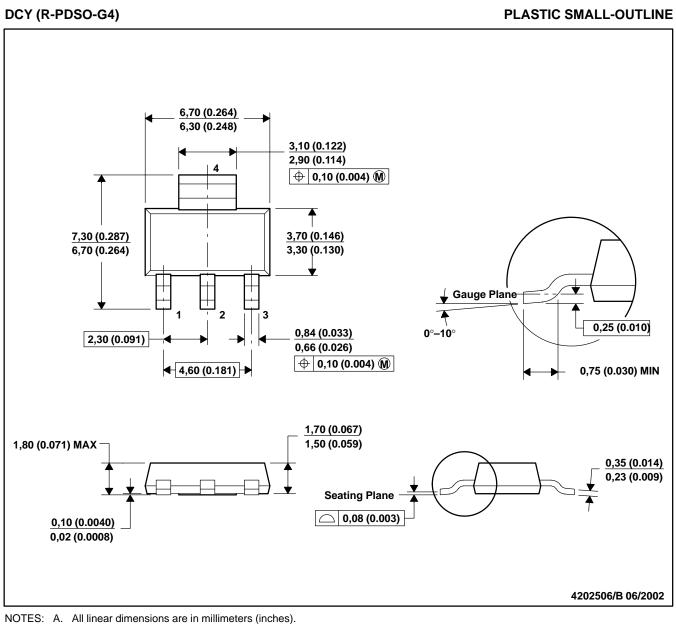
- D. C<sub>ADJ</sub> is used to improve ripple rejection; it prevents amplification of the ripple as the output voltage is adjusted higher. If C<sub>ADJ</sub> is used, it is best to include protection diodes.
- E. If the input is shorted to ground during a fault condition, protection diodes provide measures to prevent the possibility of external capacitors discharging through low-impedance paths in the IC. By providing low-impedance discharge paths for C<sub>O</sub> and C<sub>ADJ</sub>, respectively, D1 and D2 prevent the capacitors from discharging into the output of the regulator.

#### Figure 14. Adjustable Voltage Regulator



# **MECHANICAL DATA**

MPDS094A - APRIL 2001 - REVISED JUNE 2002



- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion.
- D. Falls within JEDEC TO-261 Variation AA.

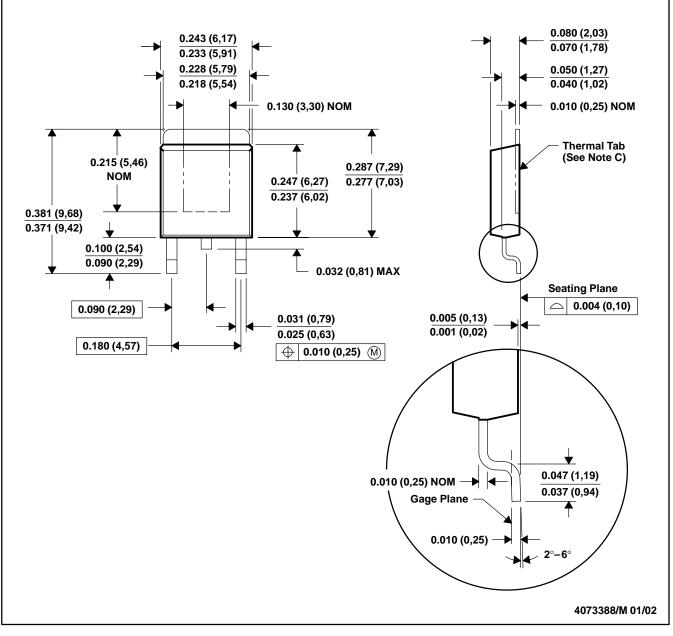


# **MECHANICAL DATA**

MPSF001F - JANUARY 1996 - REVISED JANUARY 2002

#### KTP (R-PSFM-G2)

#### PowerFLEX<sup>™</sup> PLASTIC FLANGE-MOUNT PACKAGE



- NOTES: A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. The center lead is in electrical contact with the thermal tab.
  - D. Dimensions do not include mold protrusions, not to exceed 0.006 (0,15).
  - E. Falls within JEDEC TO-252 variation AC.

PowerFLEX is a trademark of Texas Instruments.

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